



**Defence Research Development Organization (DRDO)**  
**Ministry of Defence**  
**Defence Electronics Research Laboratory**

**GLOBAL NOTICE INVITING FOR EXPRESSION OF INTEREST (EOI)**


On behalf of the President of India, the Director, Defence Electronics Research Laboratory (DLRL) invites Expression of Interest (EOI) for the following activity:

S.No.	Reference	Description of Proposal
1.	DLRL/IMP/EOI/16-17/01	Development and supply of T/R Core chip in Enhancement/Depletion(E/D) sub 180nm, Gallium Arsenide (GaAs) Technology
2.	DLRL/IMP/EOI/16-17/02	Design, Development and supply of Wideband High Power T/R chip in Gallium Nitride (GaN) Technology.

To maintain confidentiality, the detailed scope of work would be available only to those interested parties, who will execute a Non-Disclosure Agreement (NDA) with DLRL. To obtain a copy of the NDA, please send email request to [dlrlpost@dlrl.drdo.in](mailto:dlrlpost@dlrl.drdo.in) mentioning the Ref no. of the EOI. The EOI and detailed proposal is to be submitted at DLRL or soft copy may be sent at above mentioned email address.

**The Last date of submission of EOI is 31 Mar, 2017 at 1530 hrs.**



  
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